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PNNL's
Open Community
Runtime

High Performance
Data Analytics

Whole Program
Adaptive Error
Detection and
Mitigation
(AEDAM)

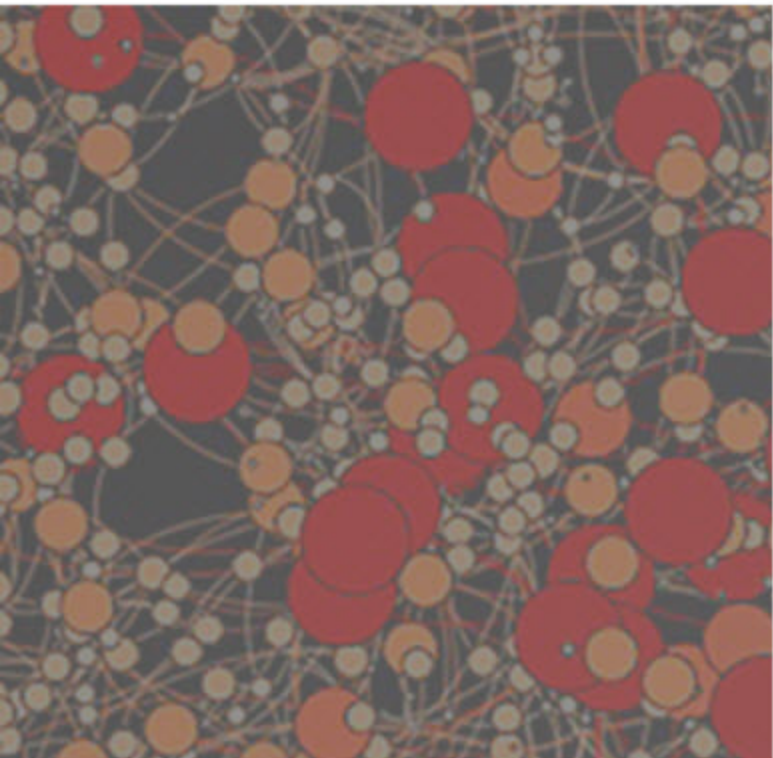
Advanced Network
Architectures:
Data Vortex

Integrated
End-to-End
Performance Prediction
and Diagnosis
for Extreme Scientific
Workflows (IPPD)

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Modeling
HPC Workload
Behavior

Assessing the
Impact of
Silicon Photonics
for Graph Applications



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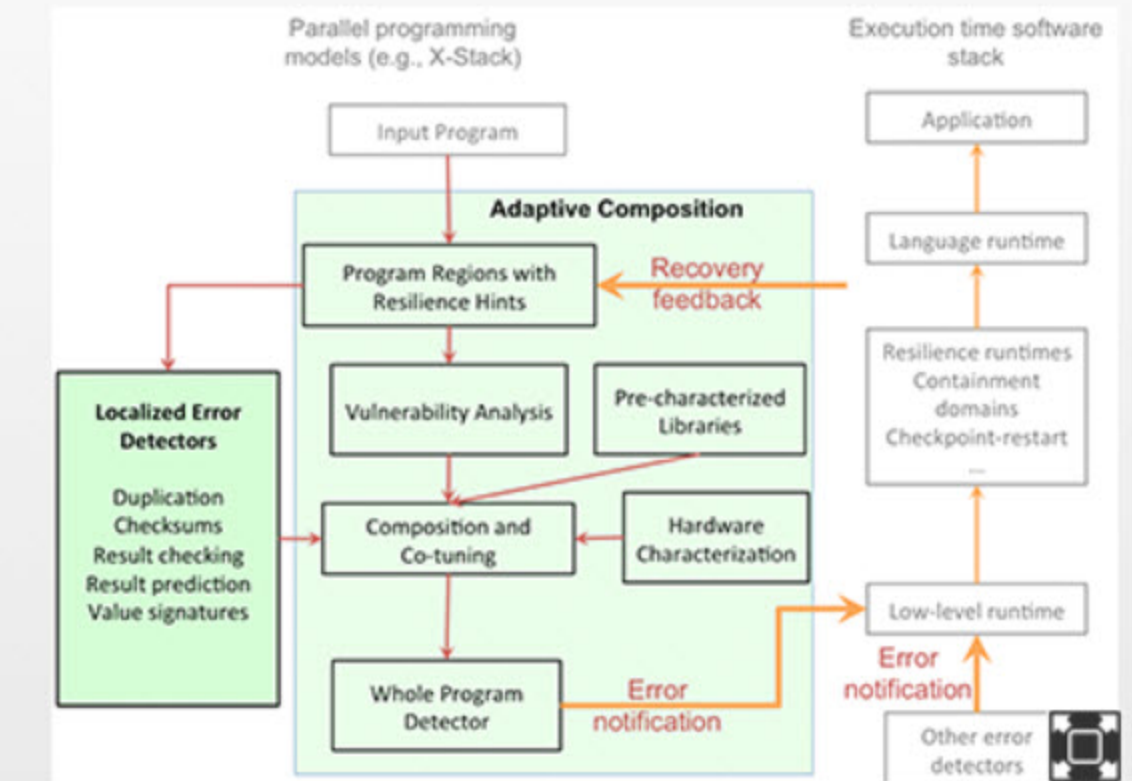
Whole Program Adaptive Error Detection and Mitigation (AEDAM)

Errors in hardware that escape detection can lead to silent data corruption. Detecting these types of errors is an important first step toward fault-tolerant program execution.

PNNL, in partnership with several leading academic research institutions, is designing a comprehensive approach to error detection and mitigation for scientific applications. The approach combines configurable error detectors, a unified reliability specification, and whole-program detector composition.

Specifically, AEDAM involves:

- Designing efficient and effective detectors for different portions of the execution state in scientific applications
- Characterizing and composing individual detectors to design an effective full-application solution
- Evaluating and understanding detector compositions
- Evaluating the impact of fault rates on the balance between hardware and software detection strategies.



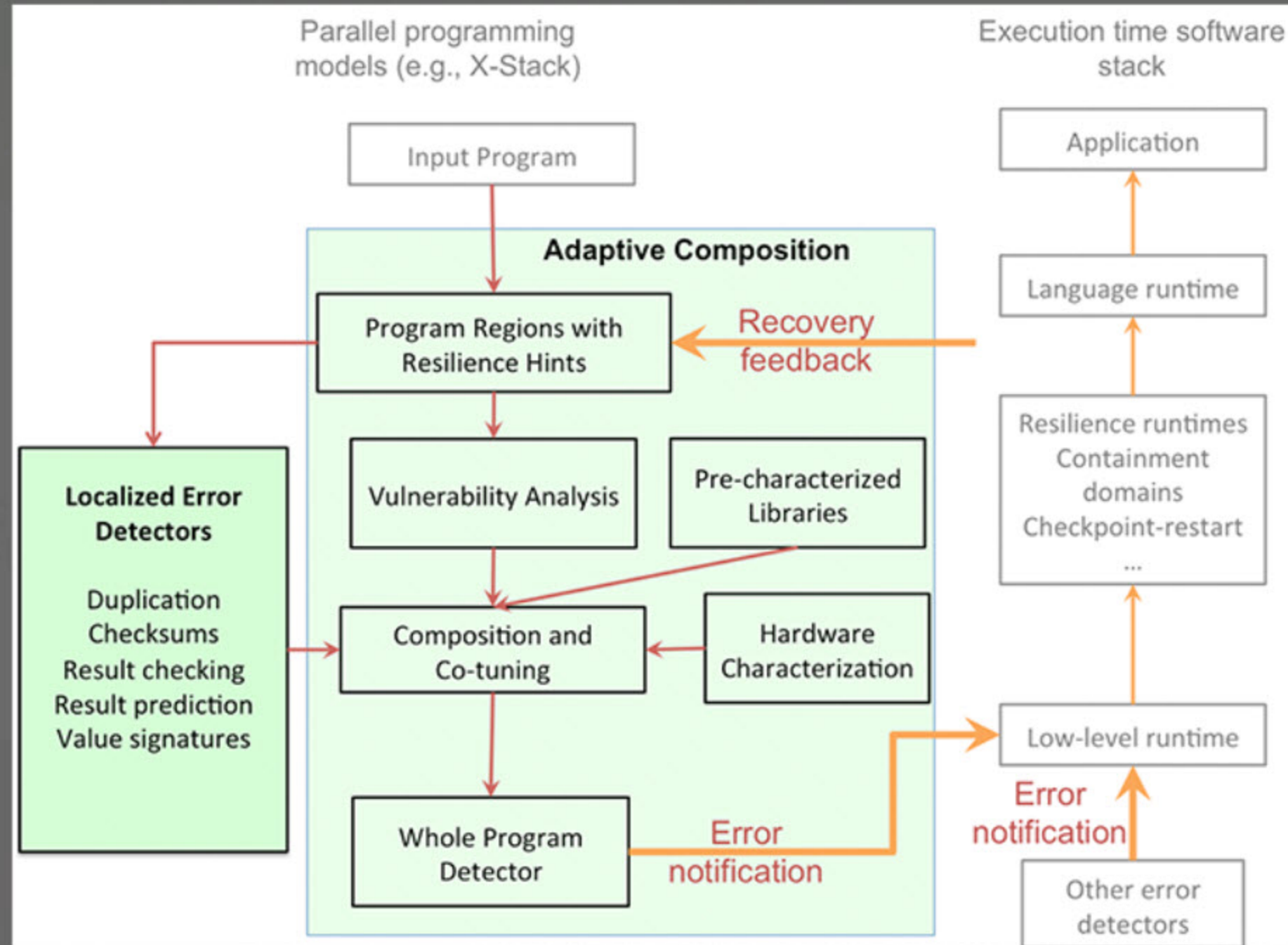
COLLABORATION

Error Detection and Mitigation

silent data corruption. Detecting these types of errors during execution.

research institutions, is designing a comprehensive application. The approach combines configurable error detection with adaptive composition.

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Funding for AEDAM is provided by the U.S. Department of Energy's Office of Advanced Scientific Computing Research. PNNL is operated by Battelle for DOE under Contract DE-AC05-76RL01830. (PNNL-SA-113784)
Contact: Sriram Krishnamoorthy, sriram@pnnl.gov

FUNDING & CREDITS

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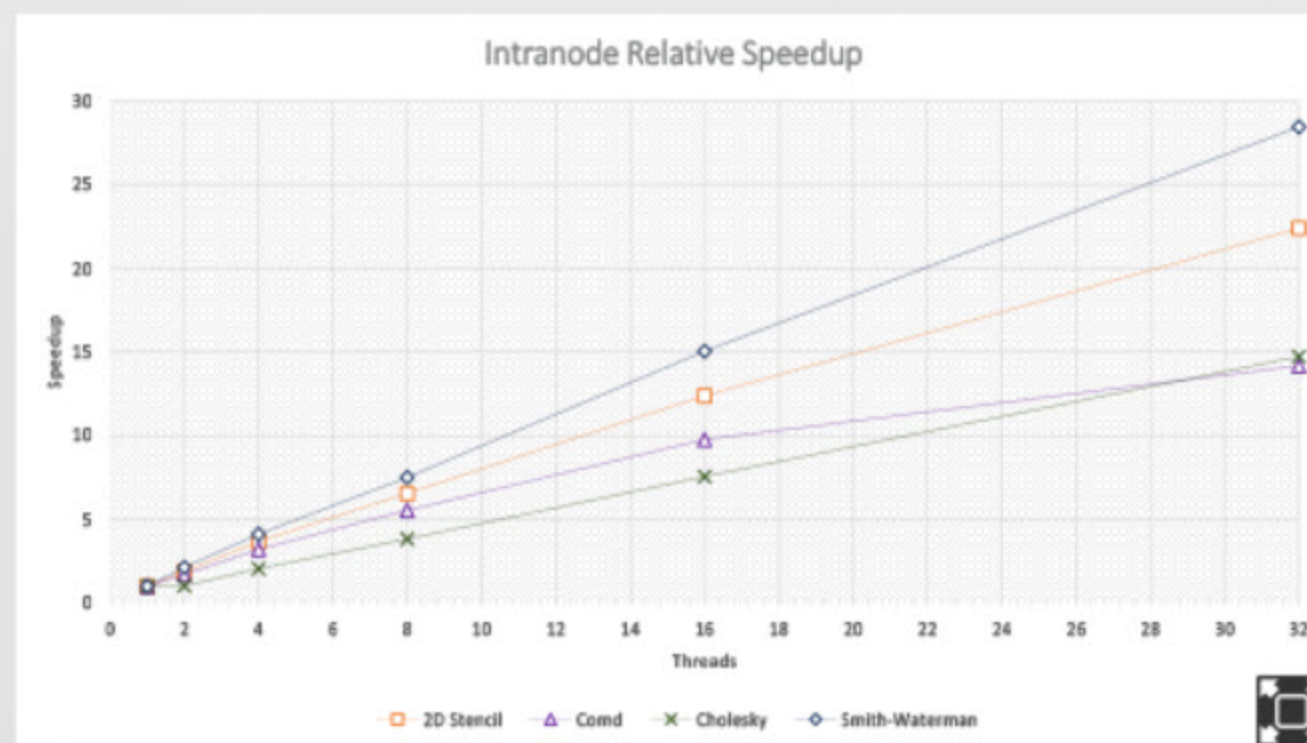
PNNL's Open Community Runtime

P-OCR: An asynchronous event-driven runtime that finally flies

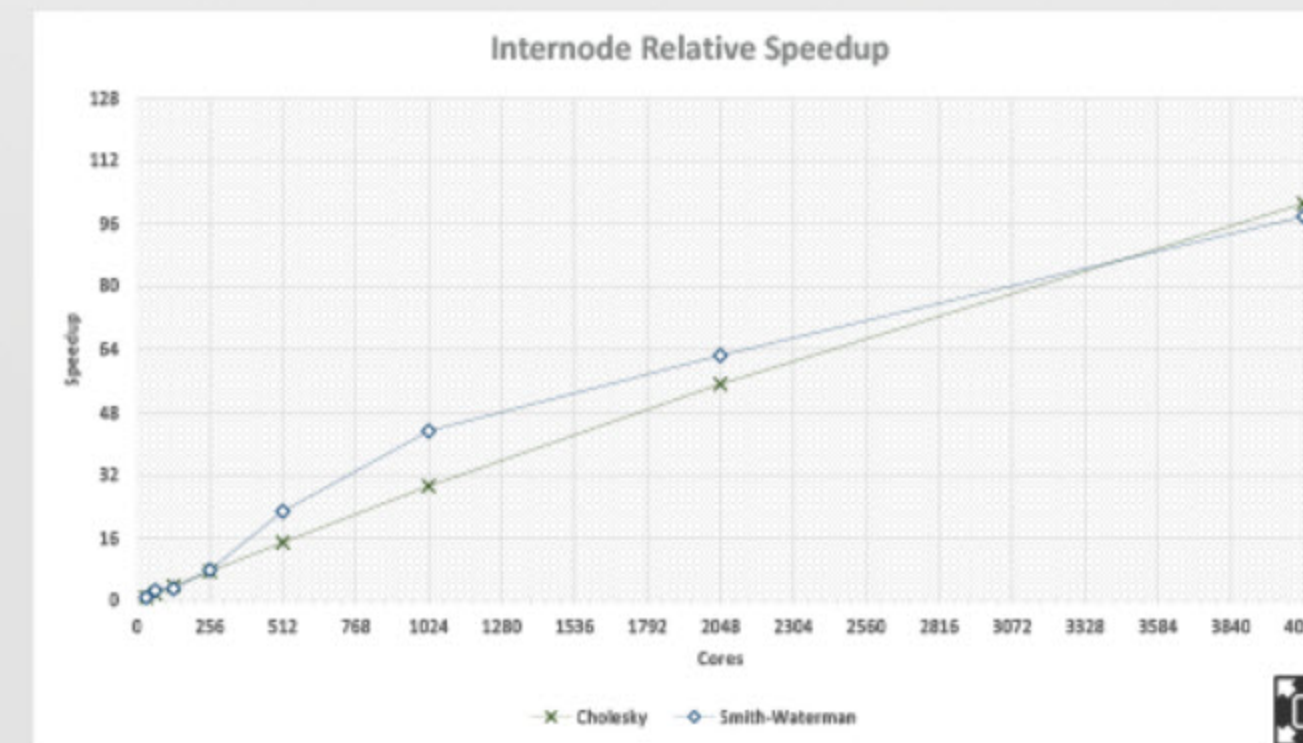
With the constraints of energy, data movement, and resiliency, future many-core architectures will impose new challenges on applications when scaling across hundreds of cores within a node and even greater challenges when scaling across thousands of these nodes.

Pacific Northwest National Laboratory's Open Community Runtime, or P-OCR, is a framework that implements the OCR standard across current distributed systems, exploring how fine-grained, event-driven tasks; data movement; and dynamic resource adaption provide scalability for regular and irregular applications while managing those constraints.

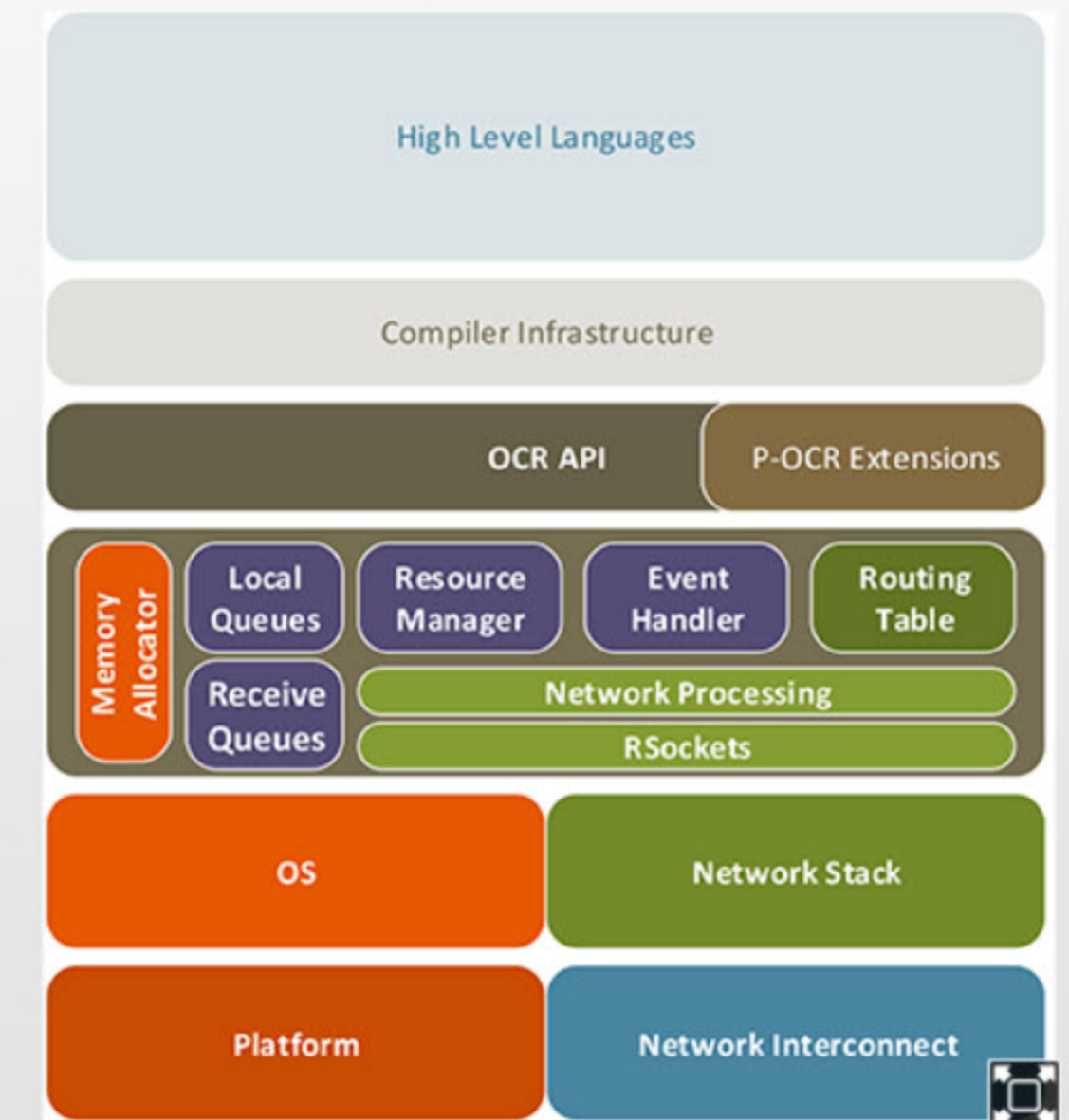
With P-OCR on a general-purpose cluster, we have demonstrated up to 1592x speedup for a parallel-tiled Cholesky kernel and 2380x speedup for a parallel-tiled Smith-Waterman kernel using 4096 cores.



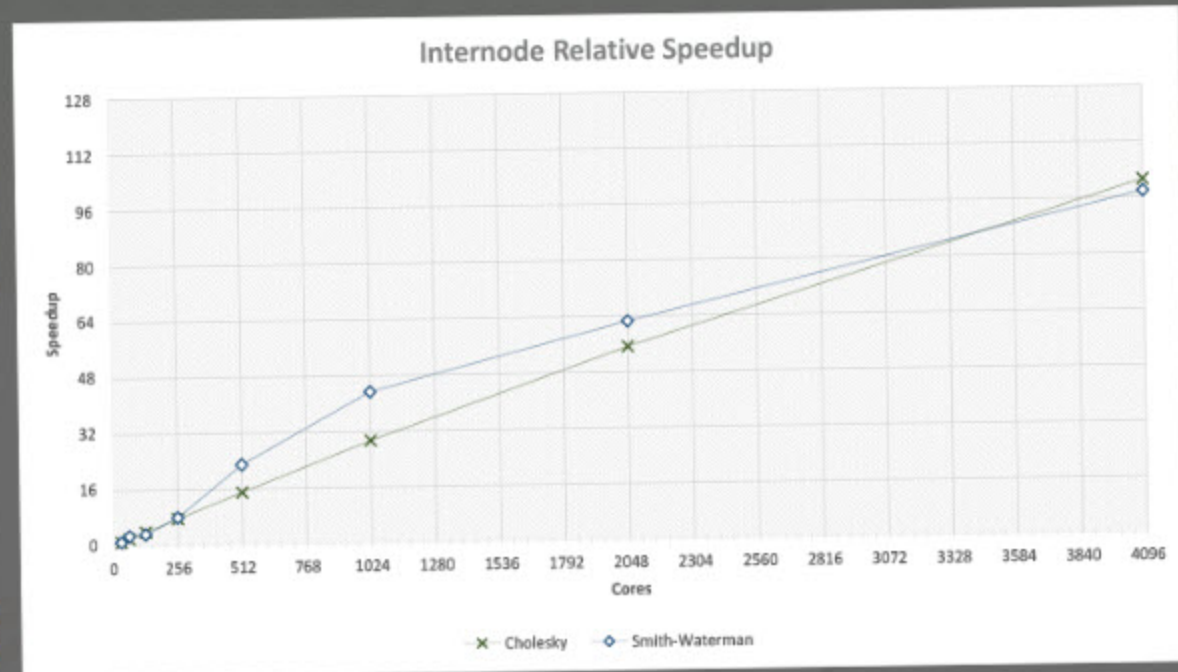
P-OCR scales within a node using shared memory* and multiple threads for both irregular and regular applications (*every two cores share a floating point unit).



P-OCR scales across nodes using Rsockets and InfiniBand. It can achieve 100x speedup on 128 nodes over one node for irregular applications.



P-OCR is a tightly coupled multithreaded runtime that combines both distributed and shared memory features to achieve scalability within or across nodes.



P-OCR scales across nodes using Rsockets and InfiniBand. It can achieve 100x speedup on 128 nodes over one node for irregular applications.

Community Run

event-driven run

data movement, and resili

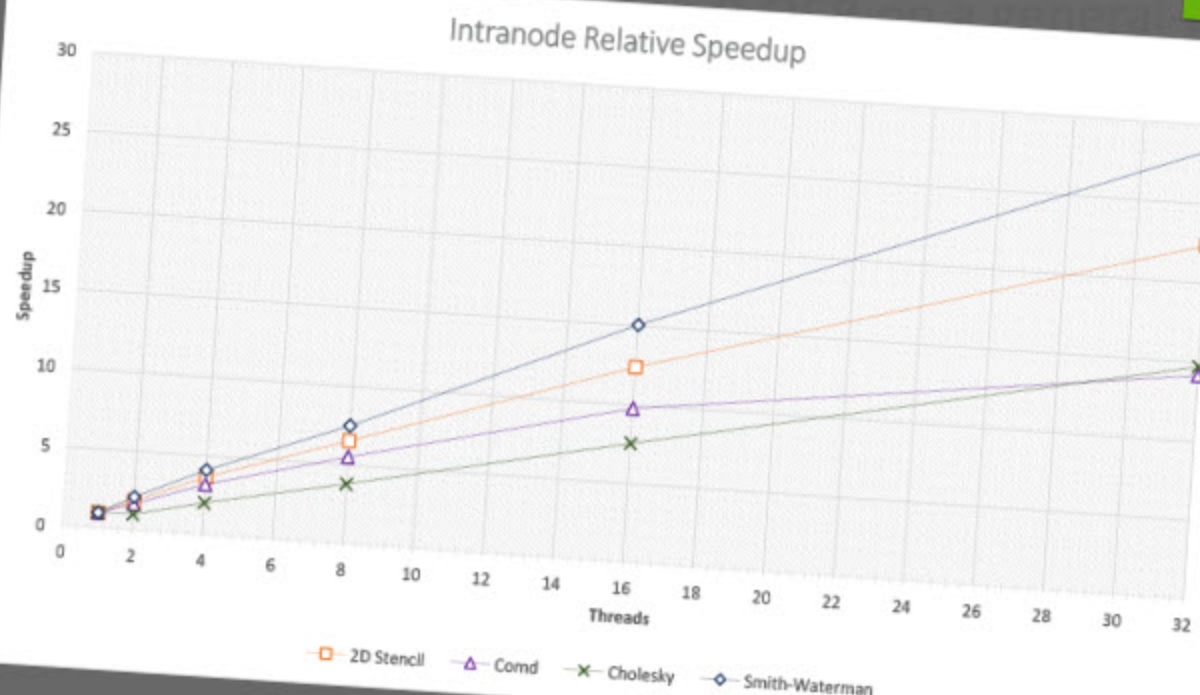
challenges on applications when scaling across hundreds of nodes.
scaling across thousands of these nodes.

Pacific Northwest National Laboratory's Open Community standard across current distributed systems, exploring dynamic resource adaption provide scalability for irregular



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a parallel-tiled Smith-Waterman

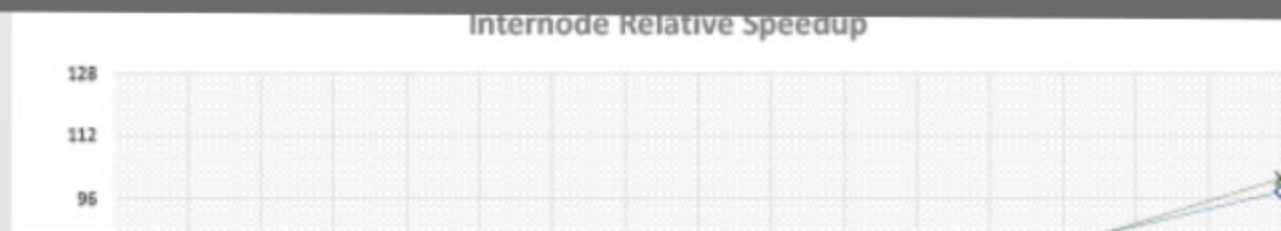
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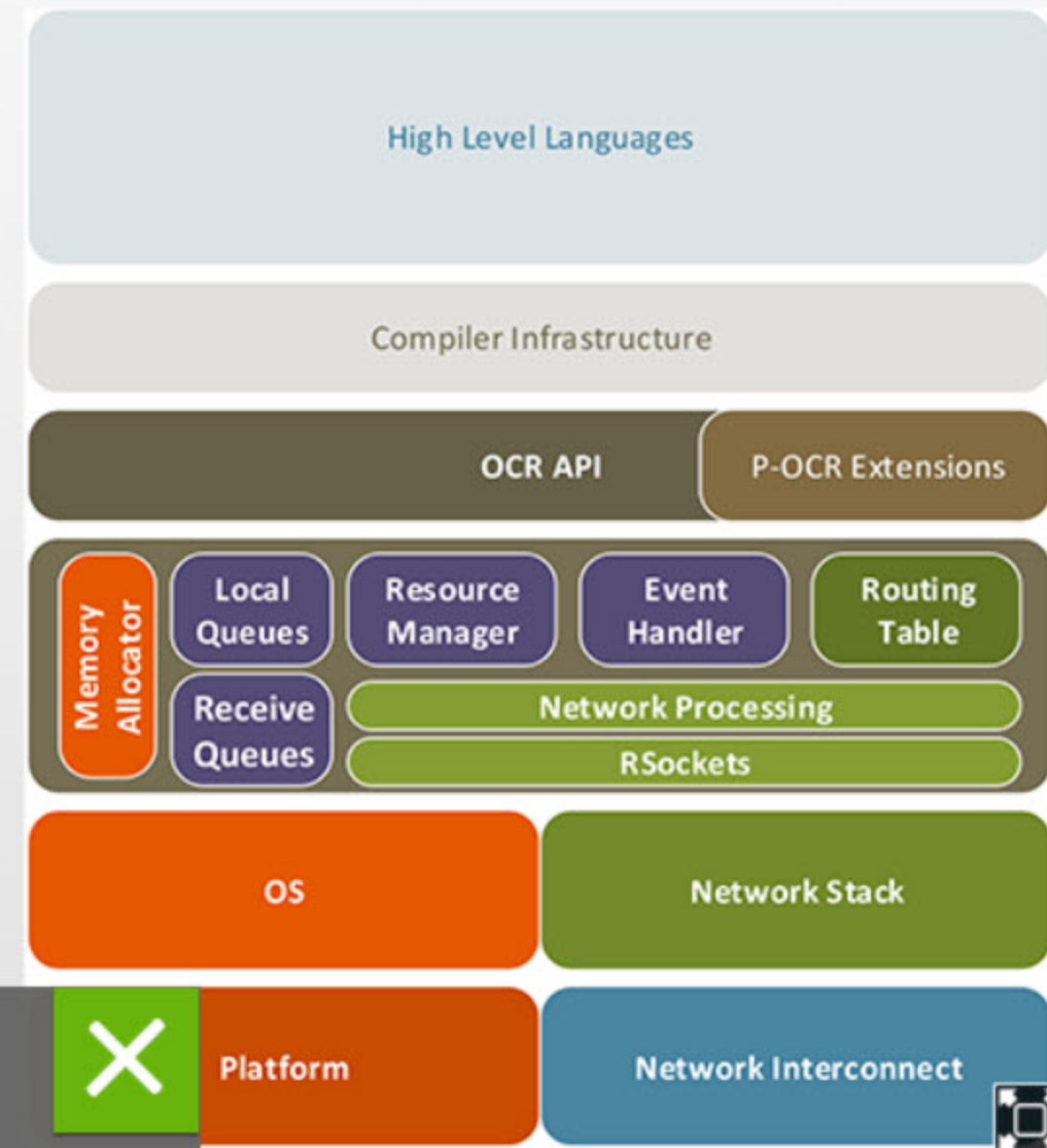
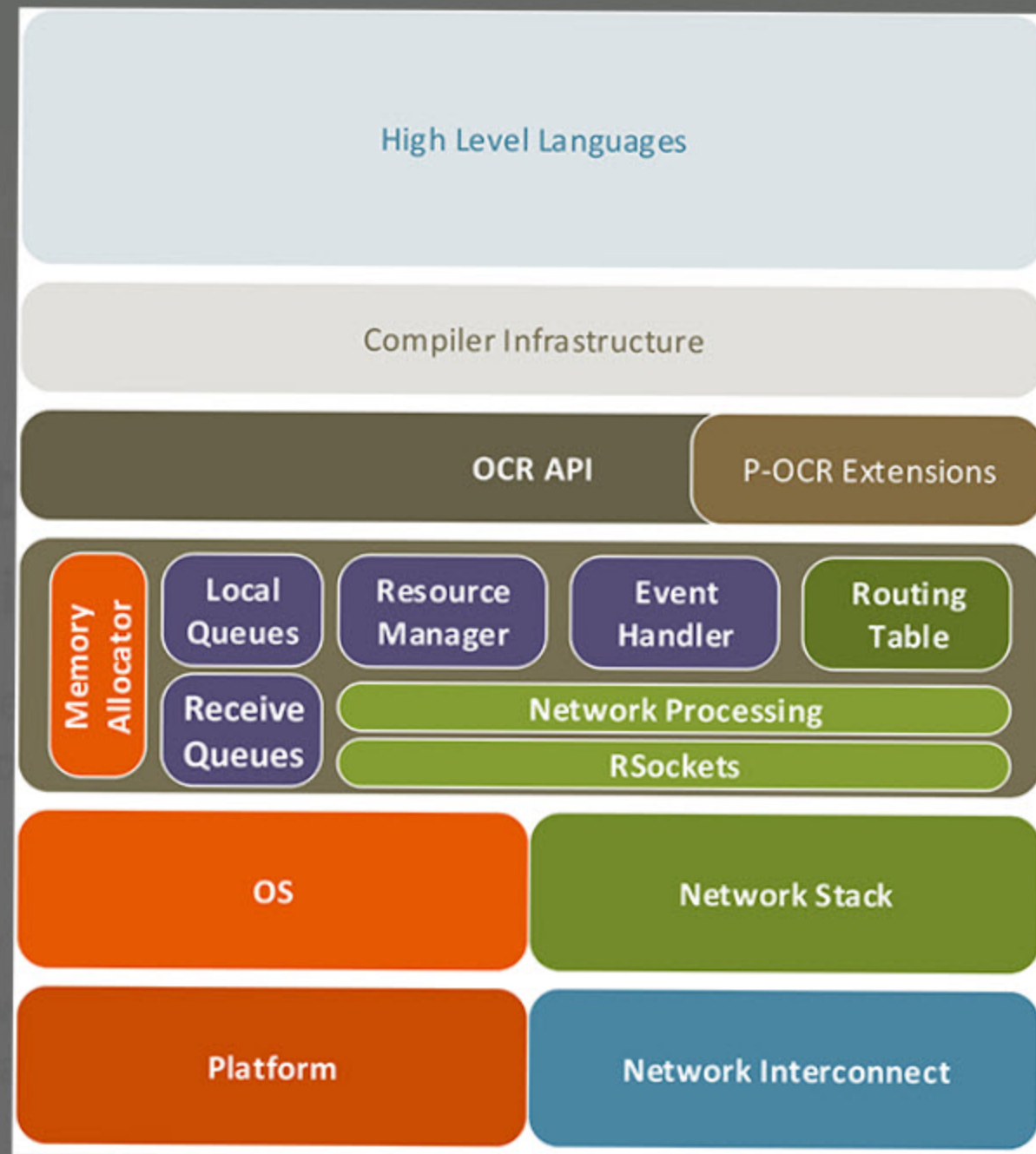
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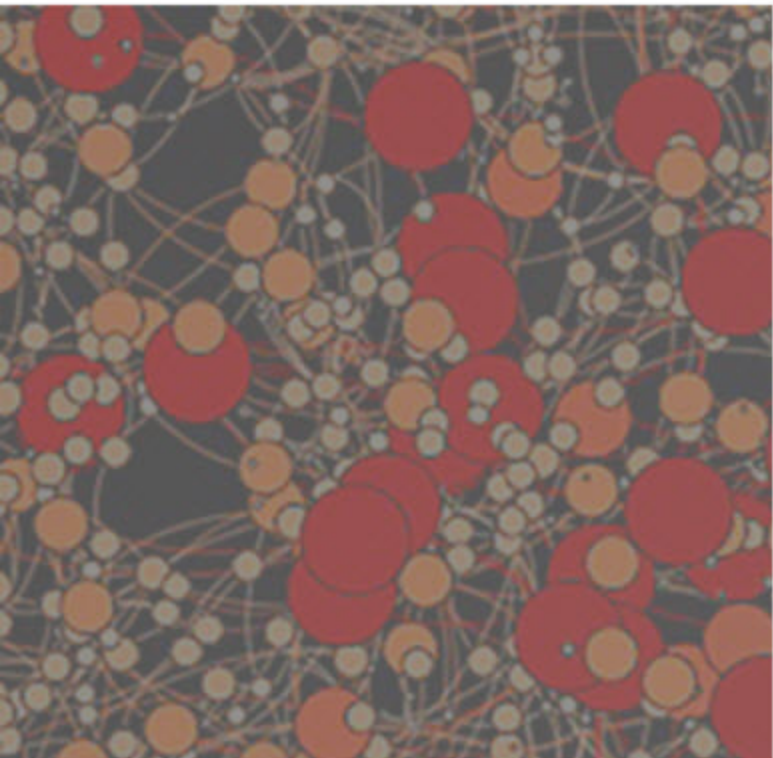


Funding for P-OCR is provided by the U.S. Department of Energy's Office of Advanced Scientific Computing Research. PNNL is operated by Battelle for DOE under Contract DE-AC05-76RL01830. (PNNL-SA-113783)
Contact: Andres Marquez, andres.marquez@pnnl.gov

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Advanced Network Architectures: Data Vortex

Pacific Northwest National Laboratory is the first site to install a Data Vortex system.

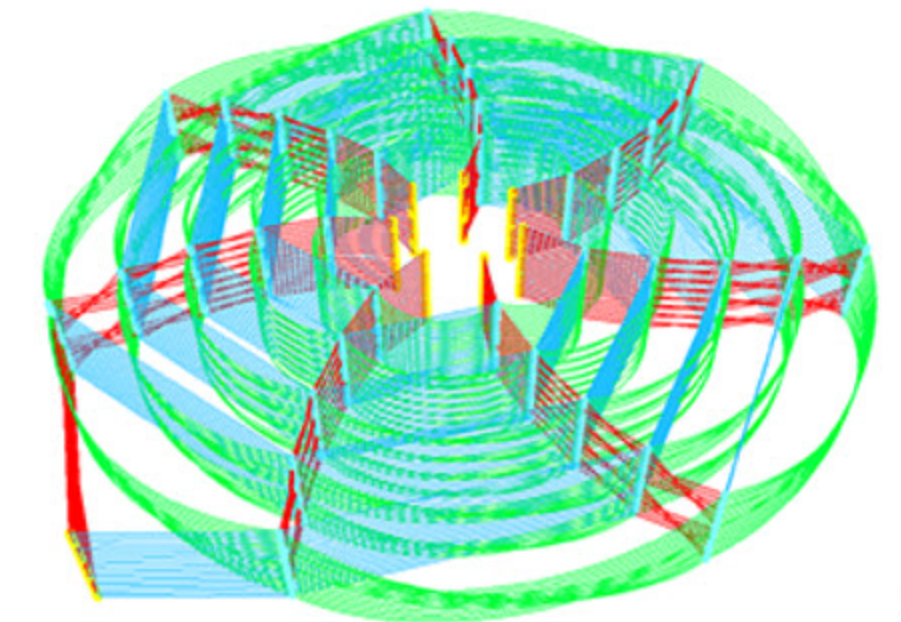
The DV205 system, dubbed “PEPSY,” is a 32-node cluster. Each node includes two Quad-Core Intel Xeon E5-2623 v3 at 3.0 GHz and 160 GB of RAM. The nodes are interconnected through both a conventional InfiniBand network and a custom network designed by Data Vortex. The custom design is a high-radix network with high bandwidth and low latency that supports very fine-grained transactions virtually congestion-free.

The design includes a Vortex Network Interface that implements a static random-access memory (SRAM)-based local memory and a custom network switch.

The Data Vortex Network may afford ways to build more balanced and efficient networks with applications and algorithms that present fine-grained data accesses, including Fourier analysis, sparse linear algebra, partial differential equations, and analytics on large data sets.

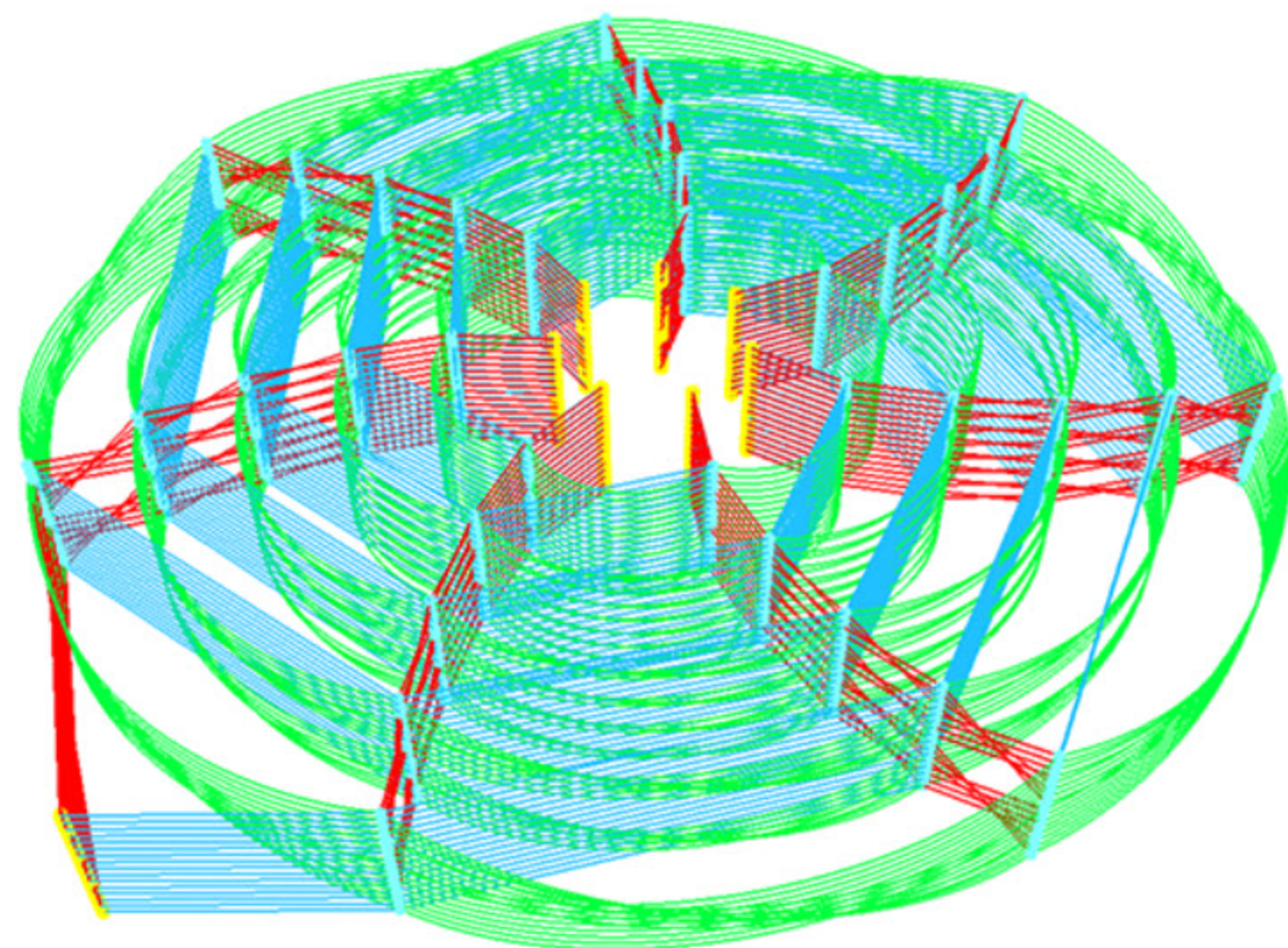
Dr. Coke Reed, chairman and founder of Texas-based Data Vortex Technologies, invented the Data Vortex Switch design from problem 110 entered by Stanislaw Ulam in the Scottish Book. Reed jointly solved the problem, concerning the existence of a fixed point in whirlpool-like flows, with Dr. Krystyna Kuperberg in 1976.

The Data Vortex Switch Topology



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The Data Vortex Switch Topology



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This technology benefits work supported by the High Performance Data Analytics Program, a collaboration between PNNL, Mississippi State University, University of Washington, and Georgia Institute of Technology. PNNL is operated by Battelle for the U.S. Department of Energy under Contract DE-AC05-76RL01830. (PNNL-SA-113909)
Contact: David Haglin, david.haglin@pnnl.gov

COLLABO

Structures: D

site to install a Data

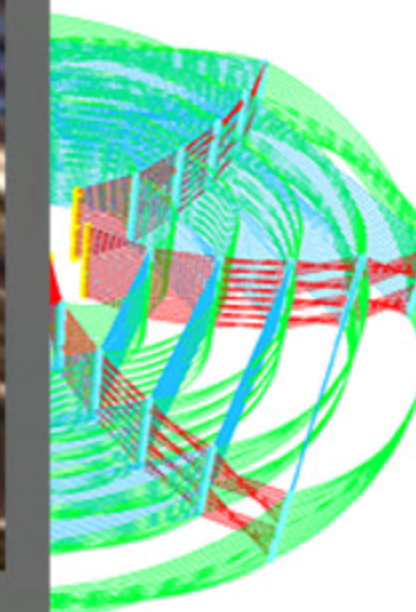
cluster. Each node is connected through a custom design is a high-density switch that implements a sta

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Switch Topology

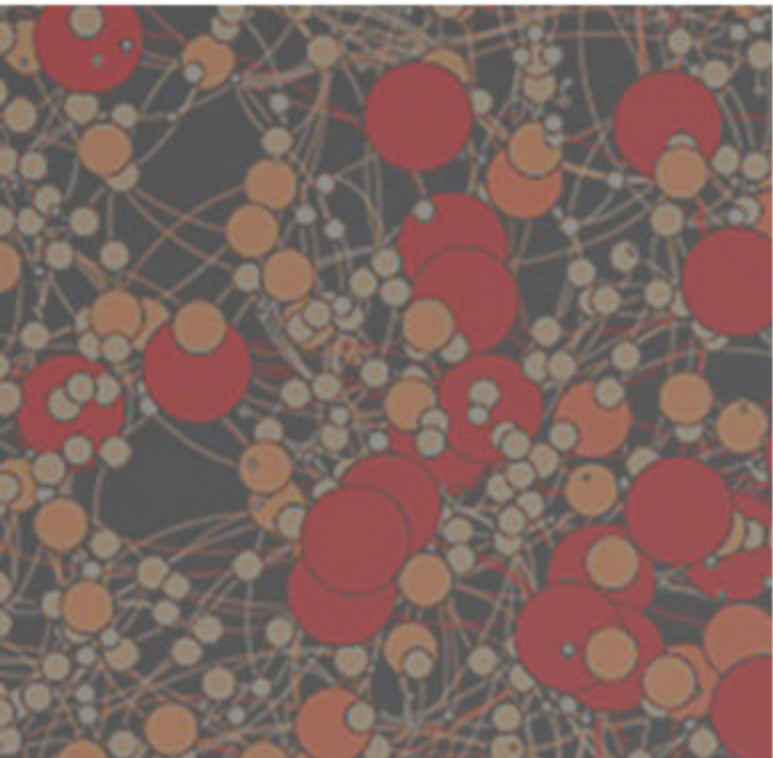


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Modeling HPC Workload Behavior

Performance Analysis of Large-Scale Systems and Applications

Application modeling captures the behavior of full-scale workloads and the interactions between these applications and the underlying hardware platform. PNNL's models allow researchers to navigate complex application and system design spaces in a rapid fashion, focusing efforts on design options that will yield the greatest benefit. The Modeling Workflow (above) can be broken into three activities:

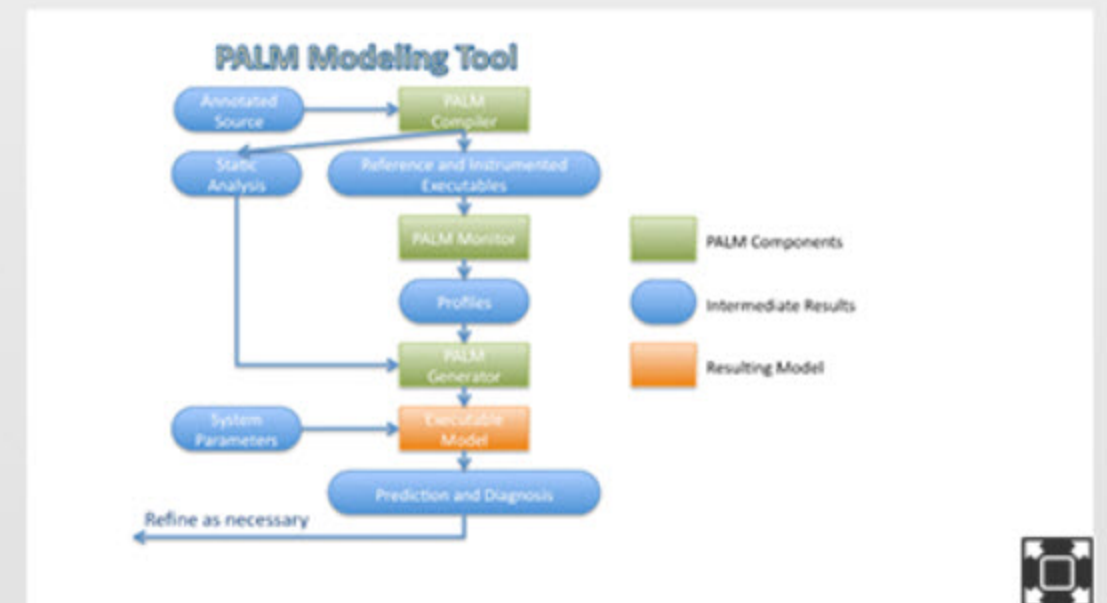
Workload and architectural specifications: targeting interesting point design studies.

Behavioral analysis and mapping: characterizing key application behaviors (e.g., computation, memory access, communication) and mapping tasks onto resources.

Prediction and validation: evaluation of models to derive predictions and validation of predictions against available measurements or simulations.

PNNL currently is advancing the state-of-the-art in modeling by developing modeling tools, applying modeling to distributed workflows, evaluating new parallel execution models, and other efforts related to U.S. Department of Energy and Department of Defense-funded programs.

Modeling requires expertise and labor that may not be available to application and system researchers. The PALM tool aims to simplify the modeling process, using an annotation language expresses insight and guides modeling. The model and application can be developed in tandem. Simultaneously, routine tasks may be automated. The resulting models are executable, reproducible, and distributable.

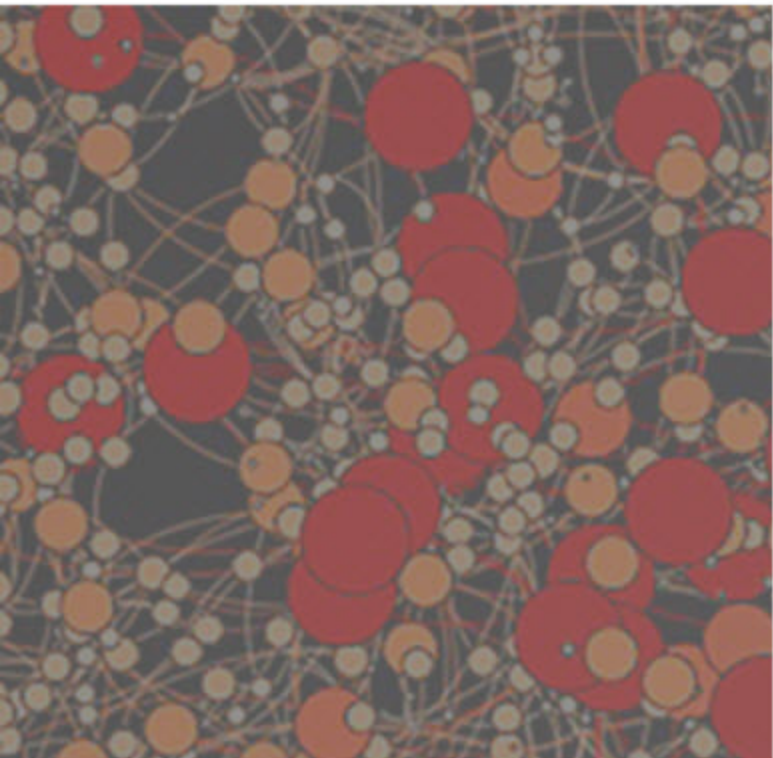




The flowchart illustrates the PALM Modeling Tool process. It begins with 'Associated Source' (blue oval) leading to 'PALM Compiler' (green rectangle). 'PALM Compiler' leads to 'Reference and Instrumented Executables' (blue oval). 'Reference and Instrumented Executables' leads to 'PALM Monitor' (green rectangle), which leads to 'Profiles' (blue oval). 'Profiles' leads to 'PALM Generator' (green rectangle), which leads to 'Executable Model' (orange rectangle). 'Executable Model' leads to 'Prediction and Diagnosis' (blue oval). 'Prediction and Diagnosis' leads to 'Refine as necessary' (blue oval), which loops back to 'Associated Source'. 'System Parameters' (blue oval) also leads to 'Executable Model'. A legend on the right indicates: Green rectangle for 'PALM Components', Blue oval for 'Intermediate Results', and Orange rectangle for 'Resulting Model'.

```

graph TD
    AS([Associated Source]) --> PC[PALM Compiler]
    PC --> RE([Reference and Instrumented Executables])
    RE --> PM[PALM Monitor]
    PM --> P([Profiles])
    P --> PG[PALM Generator]
    PG --> EM[Executable Model]
    EM --> PD([Prediction and Diagnosis])
    PD --> RA[Refine as necessary]
    RA --> AS
    SP([System Parameters]) --> EM
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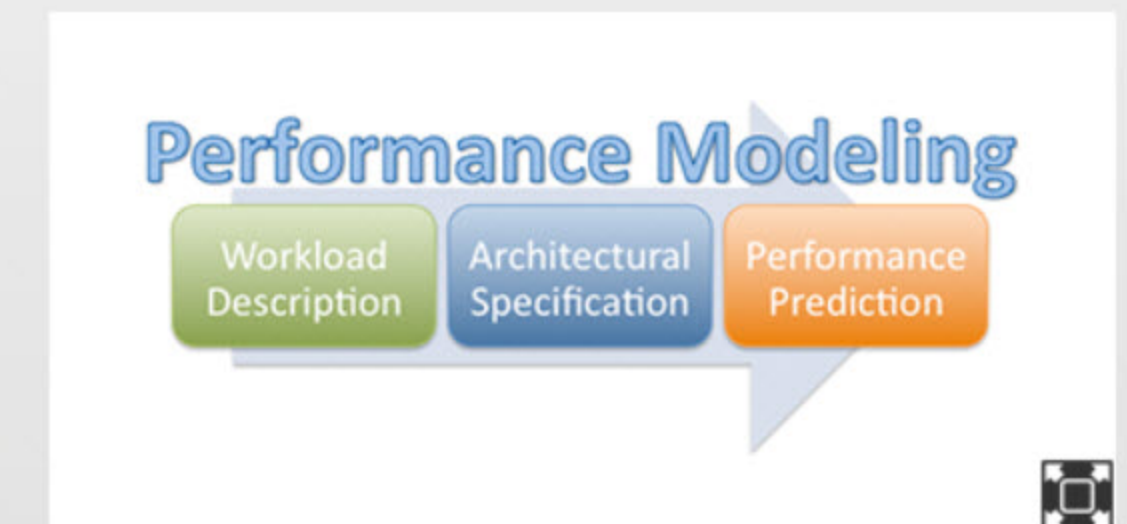
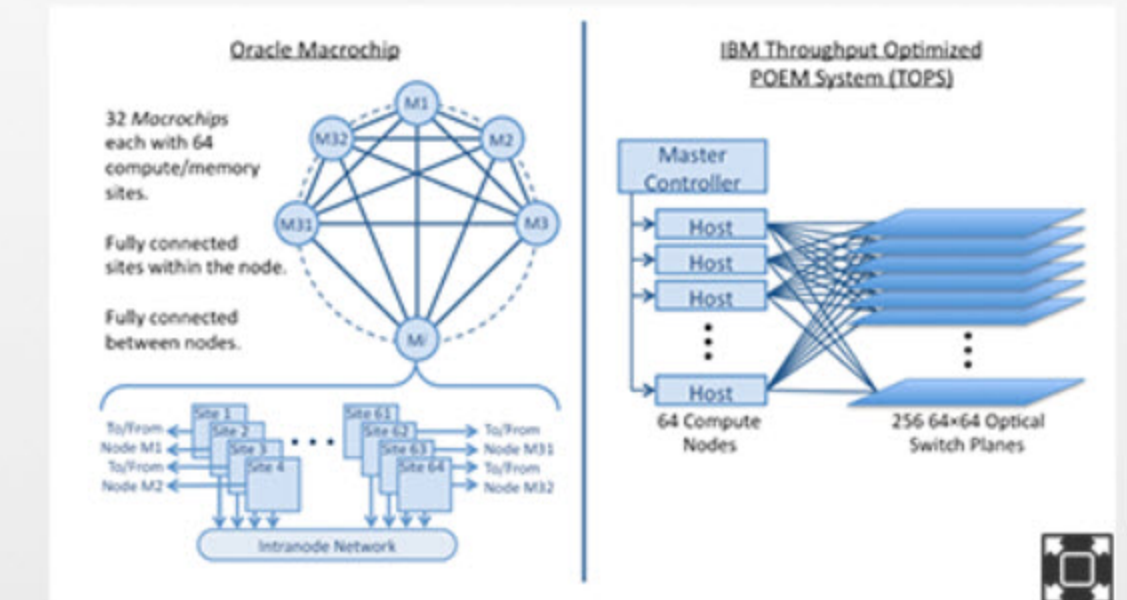
Assessing the Impact of Silicon Photonics for Graph Applications Using Performance Modeling to Evaluate Novel Technologies

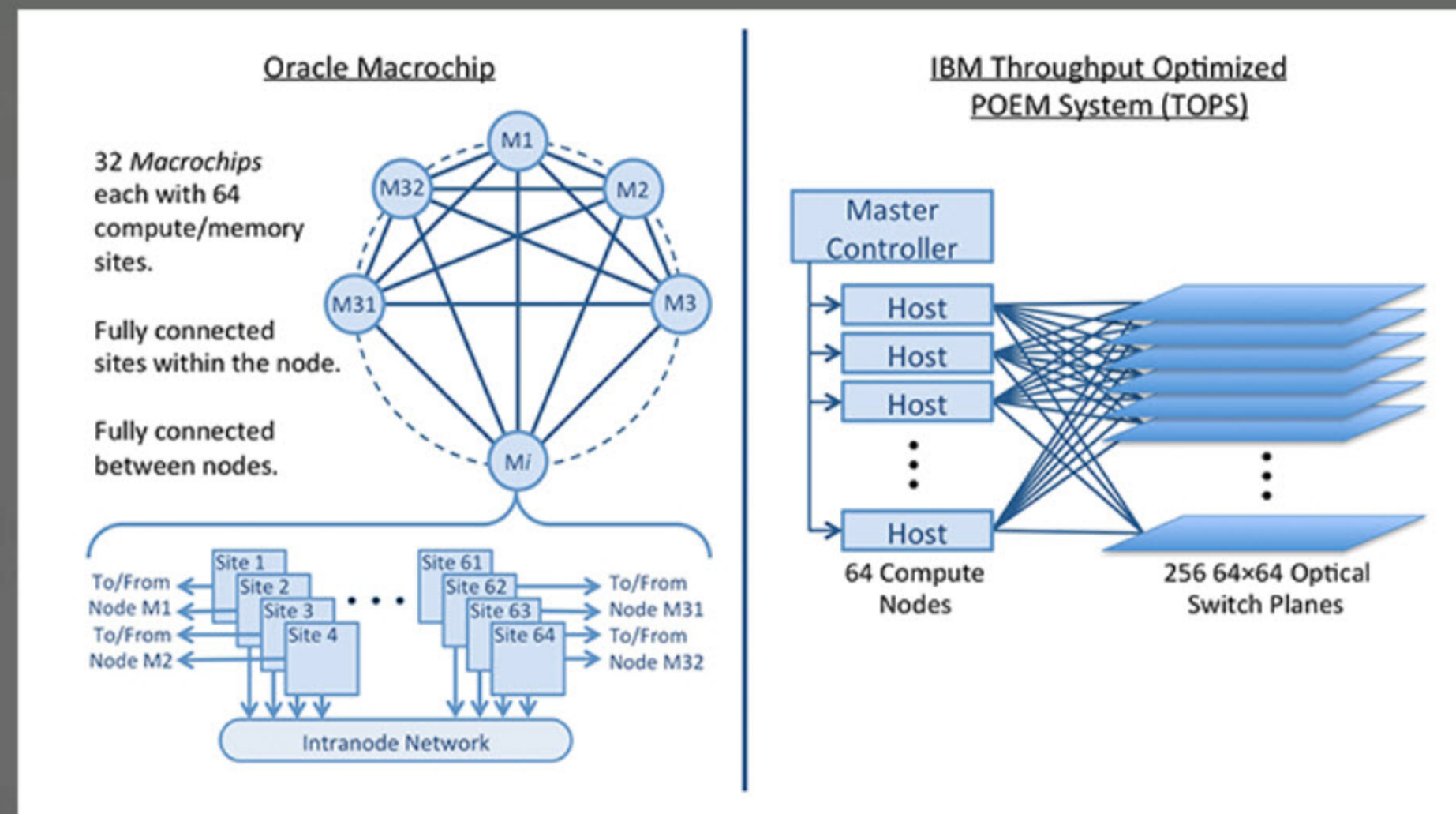
A key application of PNNL's performance modeling capability is in assessing the potential performance impact of future technologies. Silicon Photonics is on the near-term horizon and promises to deliver improved data transfer performance and energy efficiency over today's networking fabrics. PNNL has applied its performance modeling methodology to the question of whether or not these performance benefits can be realized for graph analytics applications.

Workload. PNNL's expertise yields representative examples of graph applications: Community Detection and Half-approximate Weighted Matching.

Architectures. The Oracle Macrochip employs a two-level optical fully connected network. IBM's TOPS system uses crossbar optical switch planes.

Modeling. Analytical modeling enables exploration of multiple design points in rapid succession. Workload-specific models capture key interactions between applications and architectures. By comparing and contrasting multiple designs, models can identify critical features or performance bottlenecks.





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This work was funded by the Defense Advanced Research Projects Agency under the Photonicly Optimized Embedded Microprocessors (POEM) program. PNNL is operated by Battelle for DOE under Contract DE-AC05-76RL01830. (PNNL-SA-114041)
Contact: Kevin J. Barker, kevin.barker@pnnl.gov



Specific
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Performance Modeling

Workload
Description

Architectural
Specification

Performance
Prediction



Performance Modeling

Workload
Description

Architectural
Specification

Performance
Prediction



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High Performance Data Analytics

PNNL's Multifaceted Program to Accelerate Big Data Analytics using High Performance Computing

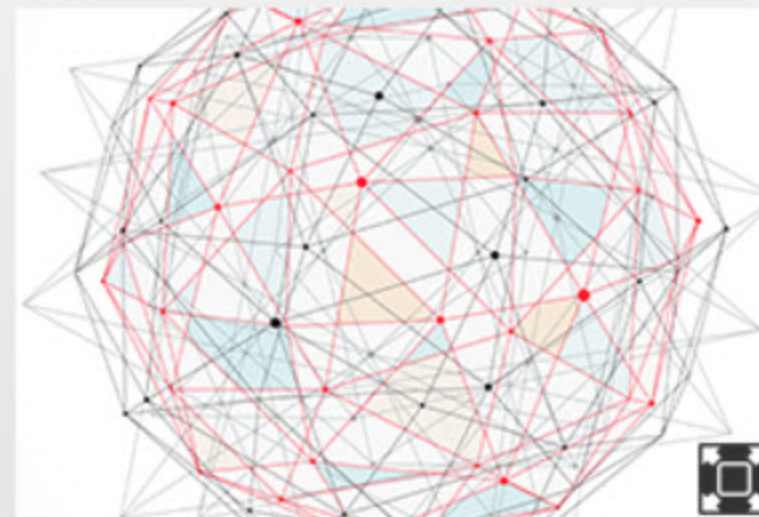
Introduced in 2013, the High Performance Data Analytics program, known as HPDA and led by Pacific Northwest National Laboratory, has been exploring, evaluating, and demonstrating the application of high-performance computing technologies to data analytics challenges.

HPDA's core focus areas include:



Streaming Analytics:

New algorithms and approaches to rapidly analyze high-bandwidth streaming data



Graph Analytics:

Graph modeling, visualization, and evaluation for understanding large, complex networks



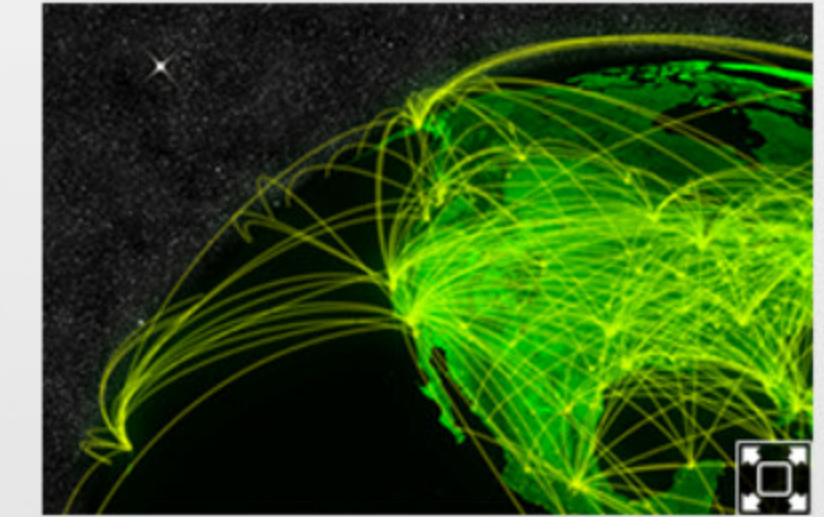
Compute Intensive Analytics:

Novel hardware architectures and innovative techniques to solve computationally intensive analysis problems



Exploratory Data Analysis:

Mechanisms to explore and analyze massive, high-velocity data sources to gain new insights and inform decisions

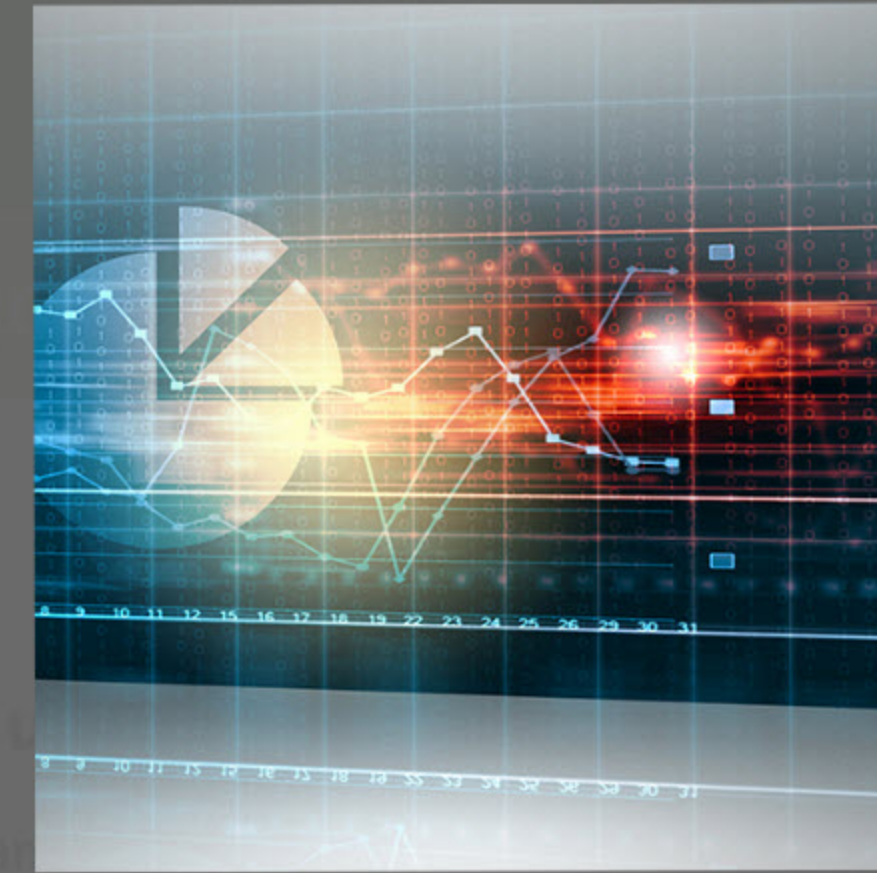
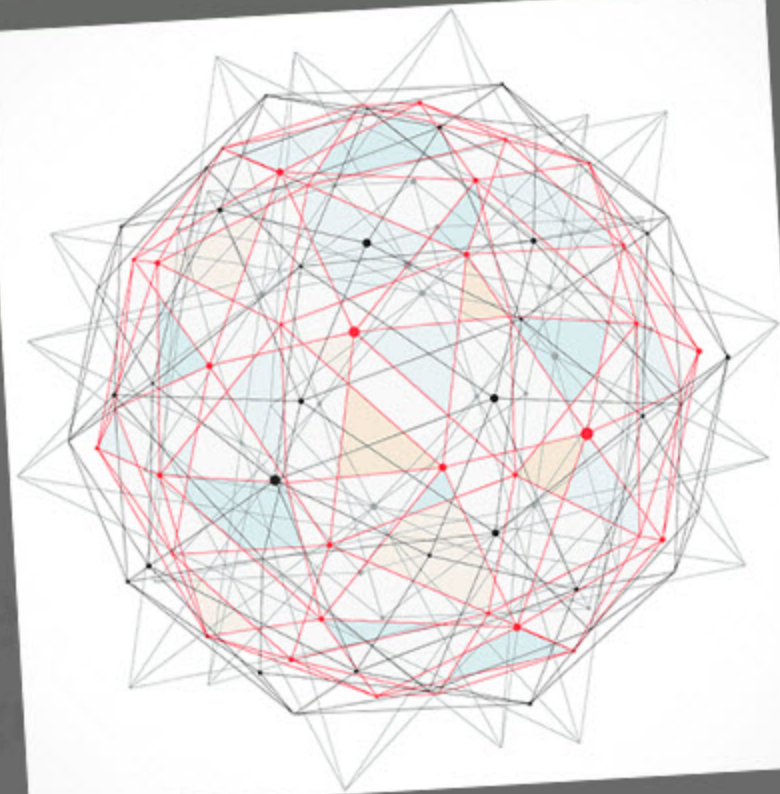


Distributed Heterogeneous HPC Testbed:

Evaluation, characterization, and modeling of emerging architectures

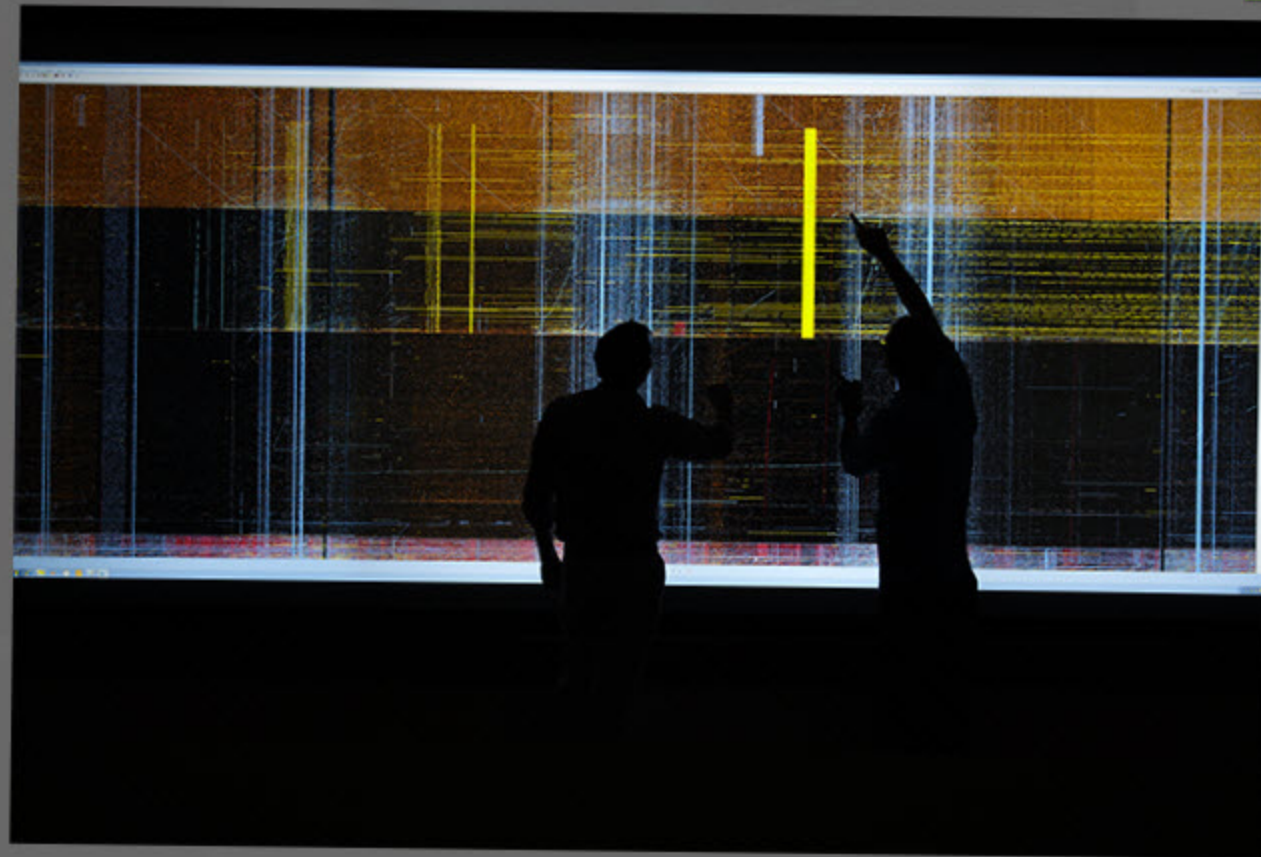
HPDA Partner Institutions





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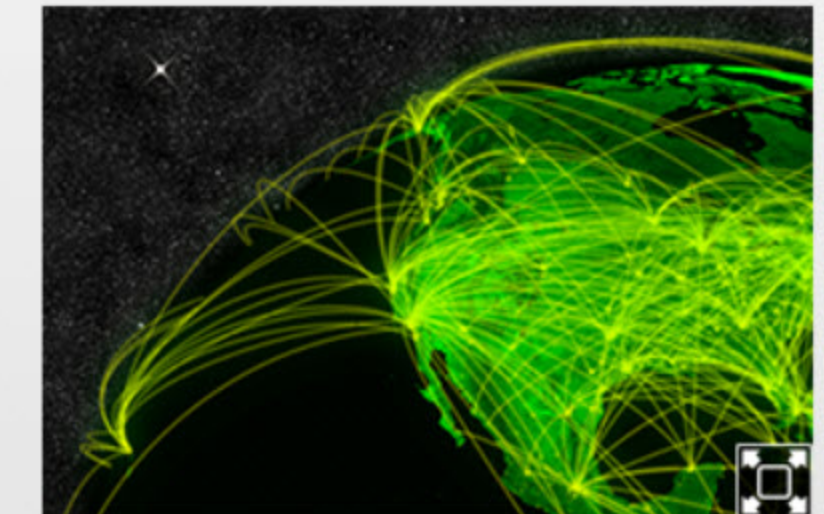
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Analytics:
modeling, v
valuation for
standing larg
rks



Core Data Analysis:
systems to explore and
analyze massive, high-velocity
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Distributed Heterogeneous HPC Testbed:
Evaluation, characterization,
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HPDA Partner Institutions



High Performance Data Analytics Program is a collaboration led by PNNL with partners Mississippi State University, University of Washington, and Georgia Institute of Technology. PNNL is operated by Battelle for the U.S. Department of Energy under Contract DE-AC05-76RL01830. (PNNL-SA-114179)
Contact: John R. Johnson, john.johnson@pnnl.gov



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Integrated End-to-End Performance Prediction and Diagnosis for Extreme Scientific Workflows (IPPD)

Using Performance Modeling to Evaluate Novel Technologies

Darren J Kerbyson (PI), Ilkay Altintas¹, Kevin Barker, Jeff Daily, Ryan Friese, Mahantesh Halappanavar, Kerstin Kleese van Dam², Mai Nguyen, Malachi Schram, Alok Singh¹, Eric Stephan, Nathan Tallent, and Jian Yin
(¹University of California, San Diego Supercomputing Center; ²Brookhaven National Laboratory)

IPPD aims to provide an integrated approach to the modeling and execution of extreme-scale scientific workflows. IPPD brings together researchers and domain scientists working on modeling and simulation, empirical analysis, optimization, workflows. IPPD builds on existing research focused on high-performance computing systems and applications and currently is using workflows from high-energy physics and climate modeling as case studies.

Modeling and Simulation. Develop modeling and simulation to enable both exploration in advance of possible workflow configuration and optimizations, as well as rapid performance prediction to guide dynamic adaptation of workflows and optimization of resource utilization. We generate models from static/dynamic annotation structure using the PALM modeling tool and use black-box modeling approaches.

Provenance. Capture empirical performance information from workflows enabling baseline performance to be established, as well as to identify and diagnose variability and feed simulation, modeling, and optimization. To enable in-depth performance analysis, we extend the Open Provenance Model to capture performance information for workflows and model runs.

Advanced Techniques. Explore novel techniques for workflow optimization for both processing and data organization. We build on the unit-commitment analogy in power grids to select a cost-efficient set of resources that meet the forecasted demand and develop Pareto-optimal solutions for energy-efficient resource use. For optimal data movement and storage, we also use transparent page management and compression.

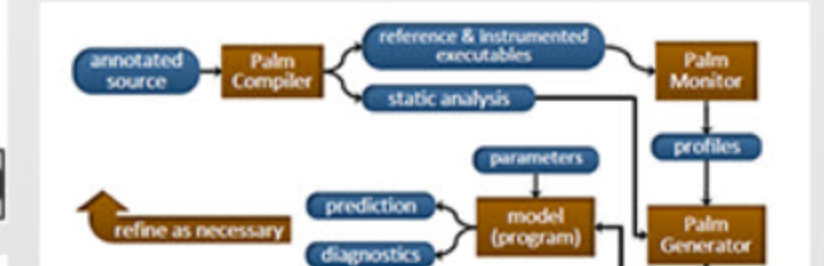
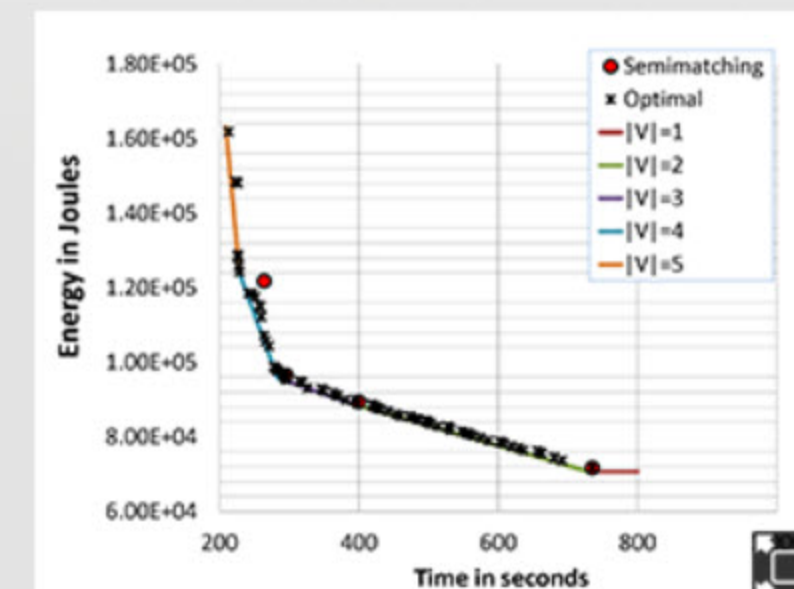
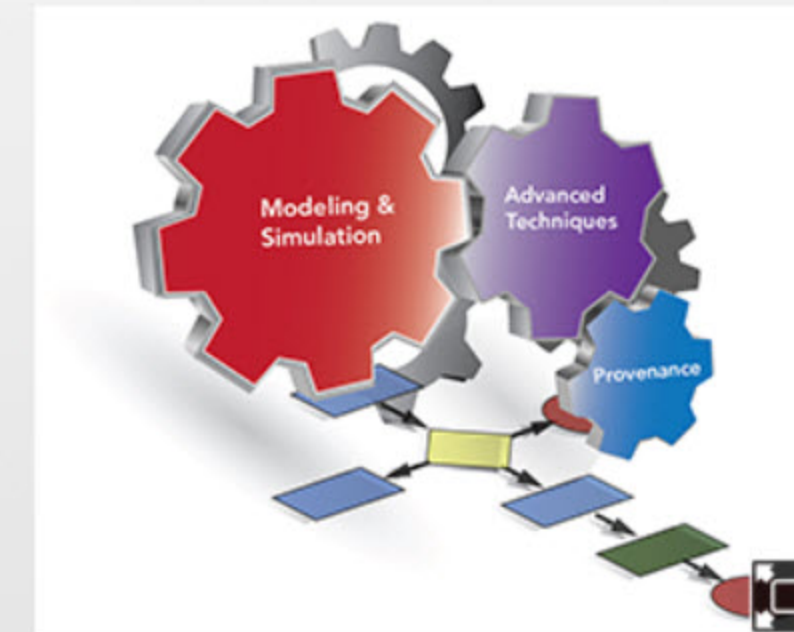


Figure 1: Workflow of the Palm modeling tool.

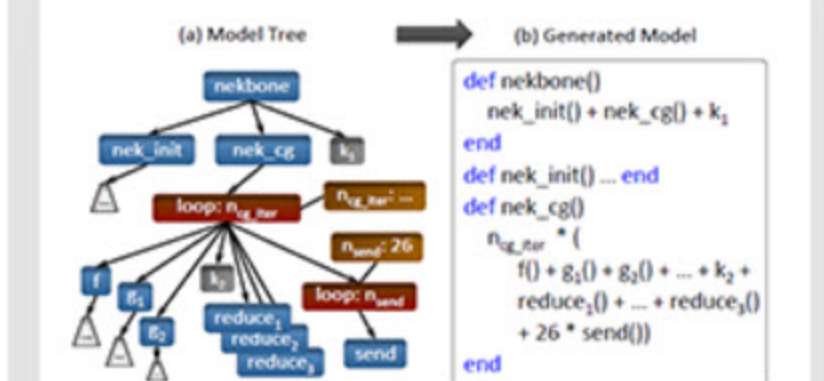


Figure 2: Palm's Generator uses (a) a Model Tree to synthesize (b) a model (a program).

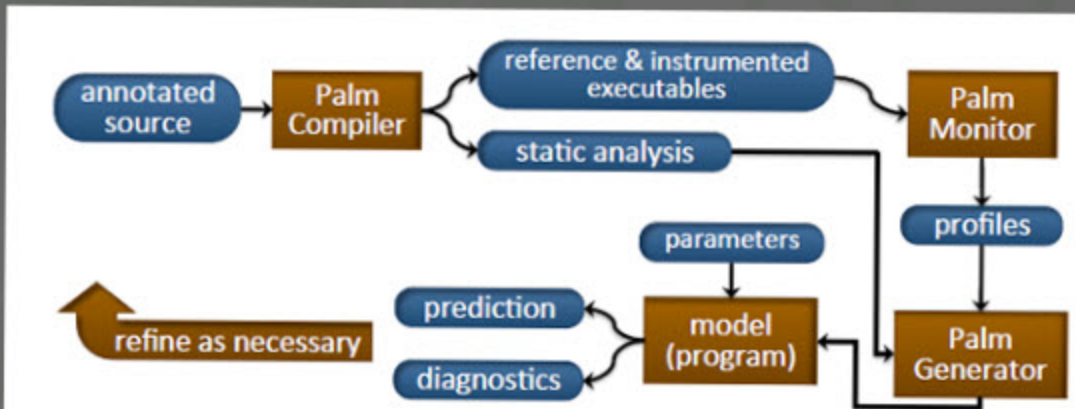
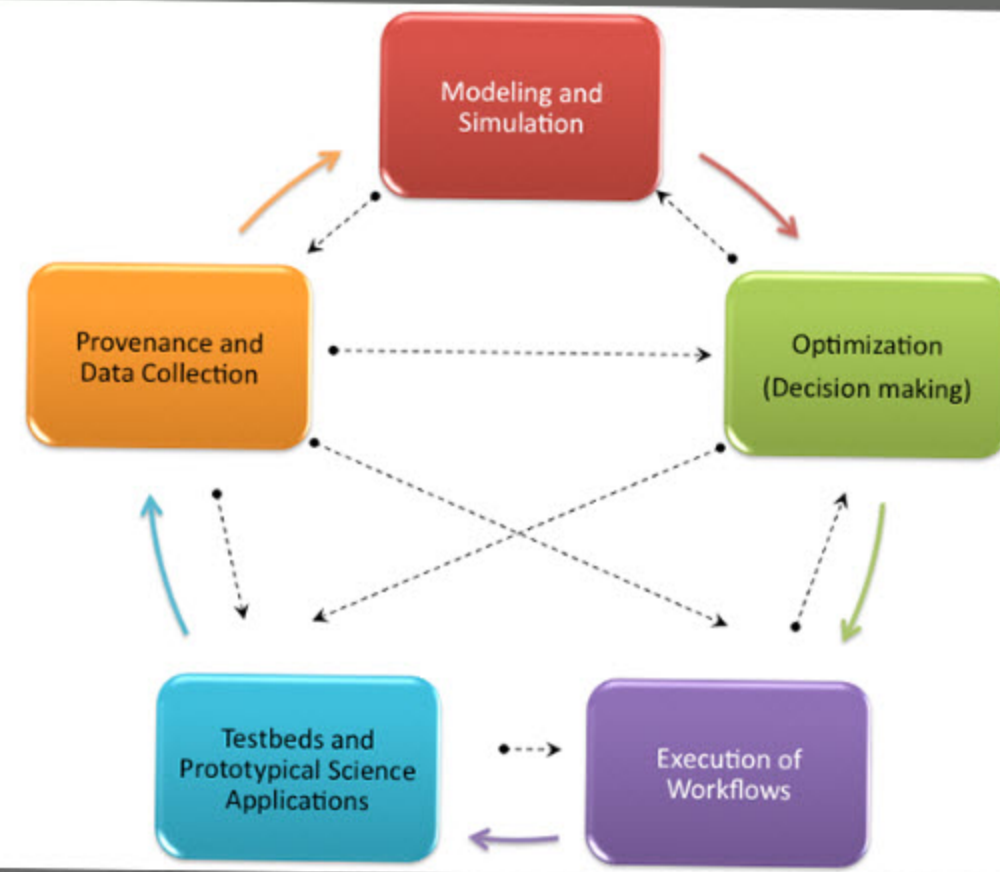
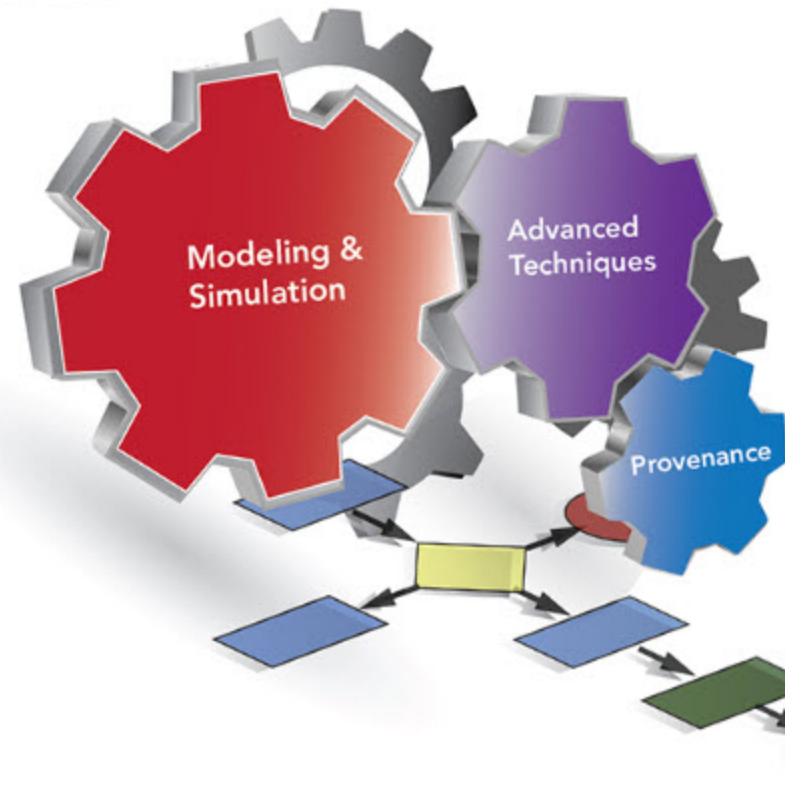


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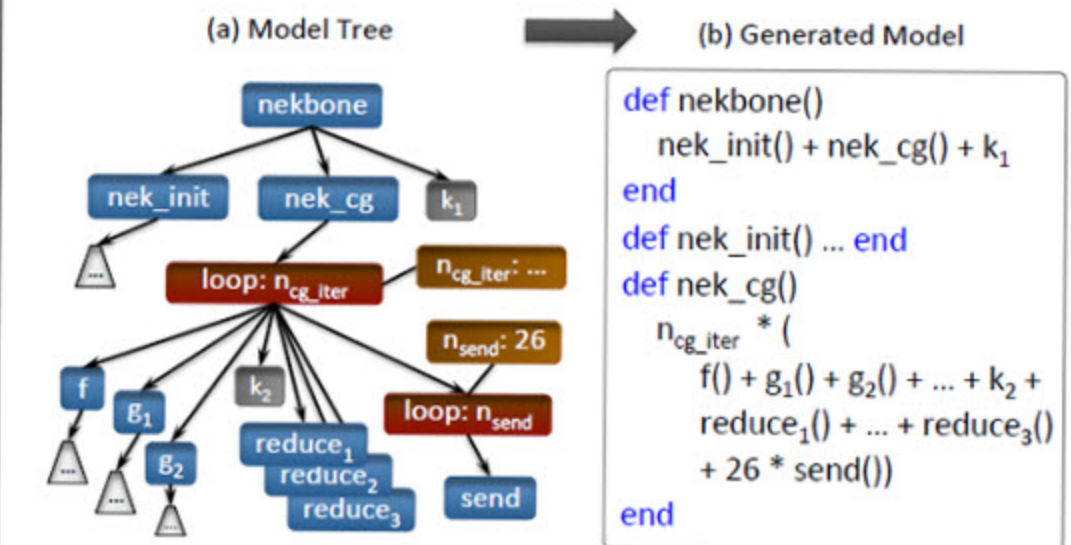


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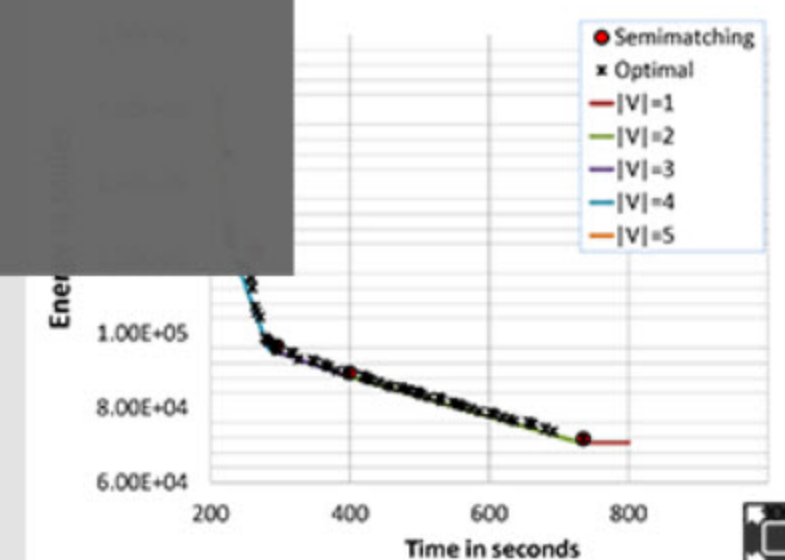
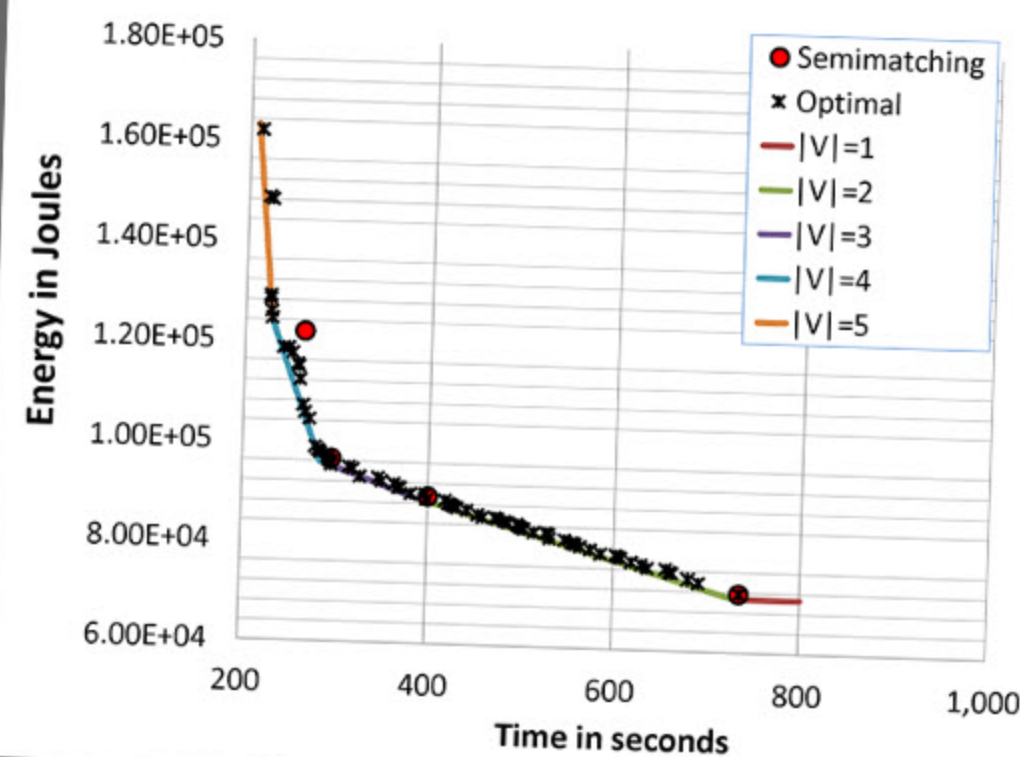


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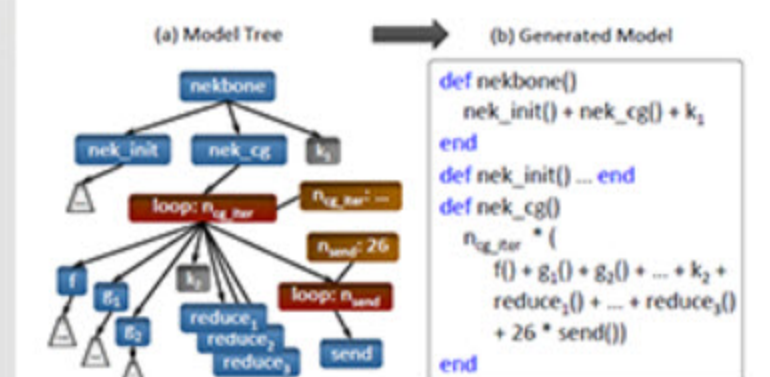


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